

Docket No. 251394US2



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Nobuyuki SATO

SERIAL NO: 10/814,290

GAU: 2811

FILED: April 1, 2004

EXAMINER:

FOR: SEMICONDUCTOR MODULE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- ☐ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☒ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the claims and drawings of the pending application(s) is attached.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

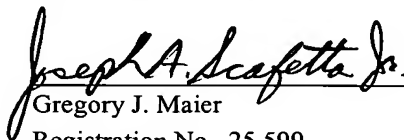
CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Gregory J. Maier

Registration No. 25,599

Joseph A. Scafetta, Jr.
Registration No. 26, 803

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 05/03)



LIST OF RELATED CASES

| <u>Docket Number</u> | <u>Serial or Patent Number</u> | <u>Filing or Issue Date</u> | <u>Inventor/ Applicant</u> |
|----------------------|------------------------------------|---------------------------------|--------------------------------|
| PER CLIENT | 10/643,717 | 08/19/03 | YANAGISAWA |
| 237753US2S | 10/438,106 | 05/15/03 | KAMEDA et al. |
| 251394US2* | 10/814,290 | 04/01/04 | SATO |

*Present Application; listed for information

EHK/sba

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DOCKET NO.: 251394US2/ams

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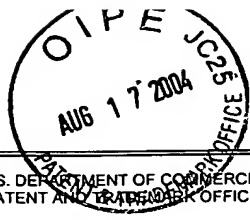
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FOR: SEMICONDUCTOR MODULE

STATEMENT OF RELEVANCY

Reference AO (JP 11-45976) on Form PTO-1449:

This reference discloses a flip-chip structure for MOSFET.

Form PTO 1449
(Modified)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO.

251394US2

SERIAL NO.

10/814,290

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Nobuyuki SATO

FILING DATE

April 1, 2004

GROUP

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS | FILING DATE IF APPROPRIATE |
|---------------------|----|--------------------|------|------|-------|--------------|-------------------------------|
| | AA | | | | | | |
| | AB | | | | | | |
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FOREIGN PATENT DOCUMENTS

| | | DOCUMENT NUMBER | DATE | COUNTRY | TRANSLATION | |
|--|----|--------------------|------------|-------------------------------|-------------|----|
| | | | | | YES | NO |
| | AO | 11-45976 | 02/16/1999 | JAPAN (with English Abstract) | | X |
| | AP | | | | | |
| | AQ | | | | | |
| | AR | | | | | |
| | AS | | | | | |
| | AT | | | | | |
| | AU | | | | | |
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

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|--|----|--|
| | AW | |
| | AX | |
| | AY | |
| | AZ | |

☐ Additional References sheet(s) attached

Examiner

Date Considered

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



FORM PTO-1449

Docket Number (Optional)
81912.0014Application Number
Not assignedINFORMATION DISCLOSURE CITATION
IN AN APPLICATION

Applicant

Satoshi YANAGISAWA

(Use several sheets if necessary)

Filing Date
August 18, 2003Group Art Unit
Not assigned

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|---------------------|-----------------|----------|--------------|-------|----------|-------------------------------|
| | 6,040,626 | 03/21/00 | Cheah et al. | | | |
| | 6,396,127 | 05/28/02 | Munoz et al. | | | |
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FOREIGN PATENT DOCUMENTS

| | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION (YES/NO) |
|--|-----------------|----------|---------|-------|----------|-------------------------|
| | 2001-358259 | 12/26/01 | Japan | | | Abstract |
| | 2002-314018 | 10/25/02 | Japan | | | Abstract |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER

DATE CONSIDERED

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